

# (jr) FPGA Logic Design Engineer (m/f/d)

(20h/week)

## PRIMARY DUTIES AND RESPONSIBILITIES

- Design and implement high-performance, concurrent and time-critical hardware architectures e.g. for camera systems
- Develop and simulate models for logic circuits such as FPGAs using VHDL and HLS
- Analyse, specify and structure system requirements
- Research, test and integrate reliable and cost-efficient hardware components
- Create documentations and schematics for systems and components
- Define interfaces of system elements and ensure their consistency

A great FPGA engineer at PLC2 Design can't be defined. Due to the fact that everyone is different there is more than one way to get the work done. But to be successful in this role, we have this kind of profile in mind:

- Bachelors degree in Computer Science, Computer Engineering, Software Engineering or other relevant field of study
- Knowledge of FPGAs
- Experience with FPGA SoCs
- Knowledge in VHDL and HLS
- Knowledge of version control software such as Git
- Experience with release management and testing
- Self-motivated, team-oriented and organized

## HIGHLY APPRECIATED QUALIFICATION AND EXPERIENCE

- Knowledge of latest AI and computer vision concepts and deployment in FPGAs
- Experience with image processing pipelines
- Experience with uboot and Linux drivers
- Experience developing with C++ and python is a plus
- Knowledge of shell scripting

## WHAT WE OFFER ADDITIONALLY TO YOU

- Individual on-the-job training
- Continuing education through PLC2 Training catalog
- Participation in field related conferences

PLC2 Design with its main office in Freiburg, is a midsize company with focus on custom developments and development of own solutions for various fields, like automotive, avionic and industry. Within this context we develop full solutions, from concept and specification, to FPGA logic design with VHDL and HLS, up to OS selection and building and SW application and until the lifecycle management and release management including the production planning. Due to our continued growth, customer demands and upcoming national and international research opportunities we are currently looking for a qualified developer available on short notice for an opportunity located in Frankfurt / Main and Endingen a. K., Germany.

## Want to join us?

We want to meet you.  
Please send your CV and a short (<1 page) motivation letter via email to

[jobs@plc2.de](mailto:jobs@plc2.de)

To learn more about PLC2 Design please visit:

<https://www.plc2.com/design>