

Workshop Essential PlanAhead

New

Being a powerful analysis and floorplanning tool, PlanAhead nowadays is packaged with all versions of the ISE software. It gets continuously developed towards an alternative to the project navigator most likely replacing it a day. At the same time, PlanAhead is the key to partial reconfiguration.

In the workshop “Essential PlanAhead” the participant learns the work flow to successfully develop and implement FPGA circuits using PlanAhead. For this purpose, at the beginning, the different project types get introduced (RTL, PinPlanning, implementation, analysis). For the further course of this training, working with HDL based projects has the main focus. Besides creating and binding RTL source files, the PinPlanning capability and working with CoreGenerator will be shown, too. During

several PC based labs, the theoretical concepts will be put to action. Along with the strategy based implementation, analysis methods such as the design rule checker (DRC) or simultaneous switching noise (SSN) analysis will be covered.

Applicable technologies

Recent FPGA technologies

Requirements

Basic knowledge of FPGA technology and the XILINX Design Tool Flow are recommended

Duration and Cost

Duration: 1 day

Cost: € 650, – net per person, including detailed training material, drinks in the breaks and lunch.

Agenda

PlanAhead Tool Project Manager

- PlanAhead Tool Design Flows
- Creating a Project
- Project Manager
- PlanAhead Tool Tips

CORE Generator Tool Integration

- Using the CORE Generator Software System
- CORE Generator Software Design Flows

I/O Pin Planning

- Using the Pin Planning Environment
- I/O Layout
- Importing and Exporting an I/O Port List
- Creating, Configuring, and Placing I/O Ports and Interfaces
- Running DRC and Noise Analysis
- Selecting Alternate Compatible Parts

Project Navigator Integration with the PlanAhead Tool