

ExpressWorkshop Express Connectivity

The Virtex-5 T-Platform allows several interface from 155 mb /s up to Gbit/s internally. There are different standard available for the different area of support. All application based on RocketIOs GTX and GTP is supported. This series of 1-day workshop teach the participant the applications based on various RocketIOs standards. Each seminar of this workshop addresses the basics of the application of standard RocketIOs. This workshop not only gives the overview of GTP and GTX links but also explains the options and interface available on these links. The complete design flow with the design implementation, simulation, verification communication will be demonstrated with example. In the final section of this workshop some

instructions will be given on how to do board design for the verification of physical communication links.

Applicable technologies

Virtex-5

Requirements

Basic Knowledge of XILINX FPGAs.

Duration and Cost

Duration: 1 day

Cost: € 600, – net per person, including detailed training material, drinks in the breaks and lunch.

Each workshop is independent and separately bookable!

Agenda

Seminars

Monday: Serial I/O with XILINX FPGAs:

- Ethernet Standards

Tuesday: Serial I/O with XILINX FPGAs:

- SDH/SONET

Wednesday: Serial I/O with XILINX FPGAs:

- Aurora

Thursday: Serial I/O with XILINX FPGAs:

- OBSAI / CPRI

Friday: Serial I/O with XILINX FPGAs:

- XAUI or SATA

Agenda for these seminars:

Standard Basics

Serial I/O Overview

Protocol Specific Design

Example Design

- Design
- Implementation
- Simulation

Application Details

- Board Design
- Link verification